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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/044,563	01/11/2002	Virgilio A. Fernandez	SC11593ZC	9564

23125 7590 04/28/2005

FREESCALE SEMICONDUCTOR, INC.  
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AUSTIN, TX 78729

EXAMINER

DU, THUAN N

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 04/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 10/044,563	Applicant(s) FERNANDEZ ET AL.	
	Examiner Thuan N. Du	Art Unit 2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 28 February 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-8 and 12-17 is/are allowed.
- 6) ☒ Claim(s) 9-11 and 18-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

*Handwritten signature*

*Handwritten mark*

### **DETAILED ACTION**

1. It is hereby acknowledged that the following papers have been received and placed of record in the file: Amendment (dated 2/28/05).
2. Claims 16-20 have been added. Claims 1-20 are presented for examination.
3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

### ***Claim Rejections - 35 USC § 103***

4. Claims 9-11 and 18-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rose (U.S. Patent No. 4,763,252) and applicant's admission of prior art [AAPA].
5. Regarding claim 9, Rose teaches a method comprising:  
receiving a clock signal (from timing circuit 40) and a trigger signal (user's command);  
in a sequence manager mode, upon receiving the trigger signal (user's command),  
allowing each of a portion of the communications to pass through to their respective destinations  
in a sequence and for a duration [col. 13, lines 41-45; col. 14, lines 45-47]; and  
in a transparent mode, allowing all of the communications to pass through to their  
respective destinations [col. 13, lines 35-37].

Rose does not explicitly detail that the memory is receiving a plurality of control signals and a plurality of enable signals. However, one of ordinary skill in the art would have recognized that the circuit taught by Rose would receive control signals and enable signals in order for the circuit to operate as described.

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More specifically, AAPA teaches a circuit which receives a plurality of control signal, a plurality of enable signals, a clock signal and a trigger signal, and upon receiving the trigger signal, allowing each of a portion of the communications to pass through to their respective destinations in a sequence and for a duration [application's specification; p. 1, lines 14-17; p. 1, line 24 to p. 2, line 4].

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to combine the teachings of Rose and AAPA because it would increase the reliability of the system by allowing the system to select the operation mode based on all the received signals.

6. Regarding claims 10 and 11, it would have been obvious to one of ordinary skill in the art to use the control bits to set the mode of operation of the circuit.

7. Regarding claim 18, AAPA teaches that the control signals are user-programmable [application's specification; p. 1, lines 26-27].

8. Regarding claims 19 and 20, AAPA teaches that control signals are stored in a memory [application's specification; p. 1, lines 24-26].

#### ***Allowable Subject Matter***

9. Claims 1-8 and 12-17 allowed.

#### ***Conclusion***

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The

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examiner can normally be reached on Monday and Wednesday-Friday: 9:30 AM - 8:00 PM, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on (571) 272-3670.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (703) 872-9306.

11. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).



Thuan N. Du  
April 22, 2005